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PATENTDocket No.: 547-131

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Pei-Pei DING et al.

U.S. Patent Application No. 10/670,190

Filed: September 26, 2003

For: METHOD OF MAKING A MICROSTRUCTURE USING A CIRCUIT BOARD

Confirmation No. 5418

Group Art Unit: 1742

Examiner: William T. LEADER

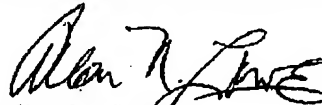
STATUS INQUIRYCommissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The applicant respectfully requests that the undersigned attorney be informed of the status of the referenced application filed on September 26, 2003.

Respectfully submitted,

LOWE HAUPTMAN &amp; BERNER, LLP

Allan M. Lowe  
Registration No. 19,6411700 Diagonal Road, Suite 300/310  
Alexandria, Virginia 22314  
703-684-1111  
Facsimile: 703-518-5499  
April 8, 2005  
AML/tal

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